

REMARKS

Claims 1-15 are pending in this application. By this Amendment, claims 1, 2, 4 and 9 are amended and claims 12-15 are new. No new matter is added by these amendments. Reconsideration of the application in view of the above amendments and the following remarks is respectfully requested.

The courtesies extended to Applicant's representative by Examiner Connolly during the telephone interview held September 4, 2007, are appreciated. The reasons presented at the interview as warranting favorable action are incorporated into the remarks below and constitute Applicant's record of the interview.

The Office Action rejects claims 1, 3, 4, 6-9 and 11 under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,779,125 to Haban in view of U.S. Patent No. 6,079,024 to Hadjimodhamnapi et al. (hereinafter "Hadji") and rejects claims 1, 3, 4, 6-9 and 11 under 35 U.S.C. §103(a) as being unpatentable over Hadji in view of Micron, High Performance With SDRAM Modules (hereinafter "Micron"). Applicant respectfully traverses these rejections.

The Office Action asserts that Haban in combination of Hadji teaches all of the features recited in claims 1, 4 and 9. As discussed during the telephone interview, Haban in combination with Hadji does not teach or suggest a semiconductor device including at least "the first first bus interface block stopping to receive a clock signal a first predetermined time elapsed after an access of the first first bus interface block to the first storage medium is completed, the second first bus interface block stopping to receive a clock signal a second predetermined time elapsed after an access of the second first bus interface block to the first storage medium is completed," as recited in amended claims 1, 4 and 9.

Hadji, in Fig. 3 and col. 4 lines 25-45, teaches the clock signal that is sent to the inactive buffers being disabled not receiving a clock signal after a predetermined time. Thus, Hadji teaches the opposite of stopping to receive a clock signal after an access of the first bus interface block to the first storage medium is completed, as recited in claims 1, 4 and 9.

Haban and Micron fail to disclose or suggest the above feature, and therefore, fail to make up for the above noted deficiencies of Hadji.

For at least the reasons above, Haban, Hadji and Micron cannot reasonably be considered to teach or to have suggested the combinations of all the features recited in at least independent claims 1, 4 and 9. Further, claims 2, 3, 5-8, 9-11 would also not have been suggested by the applied references for at least the respective dependence of these claims on allowable independent claims 1, 4 and 9, respectively, as well as for the separately patentable subject matter that each of these claims recite.

Accordingly, reconsideration and withdrawal of the 35 U.S.C. §103(a) rejections of claims 1-11 are respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-15 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



James A. Oliff

Registration No. 27,075

Tiffany J. Brooks

Registration No. 57,912

JAO:TJB/mab

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OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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